

1

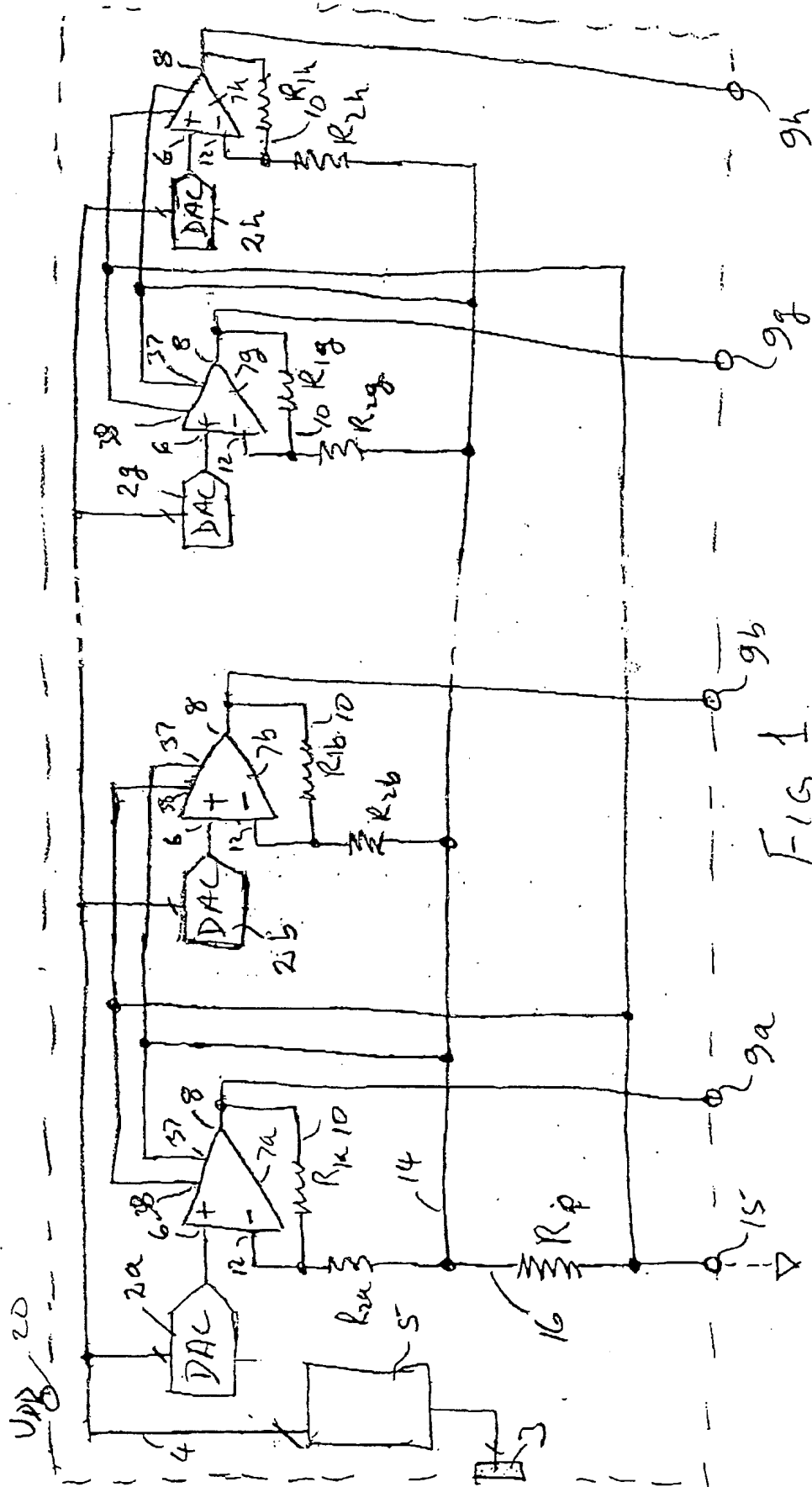


FIG 1





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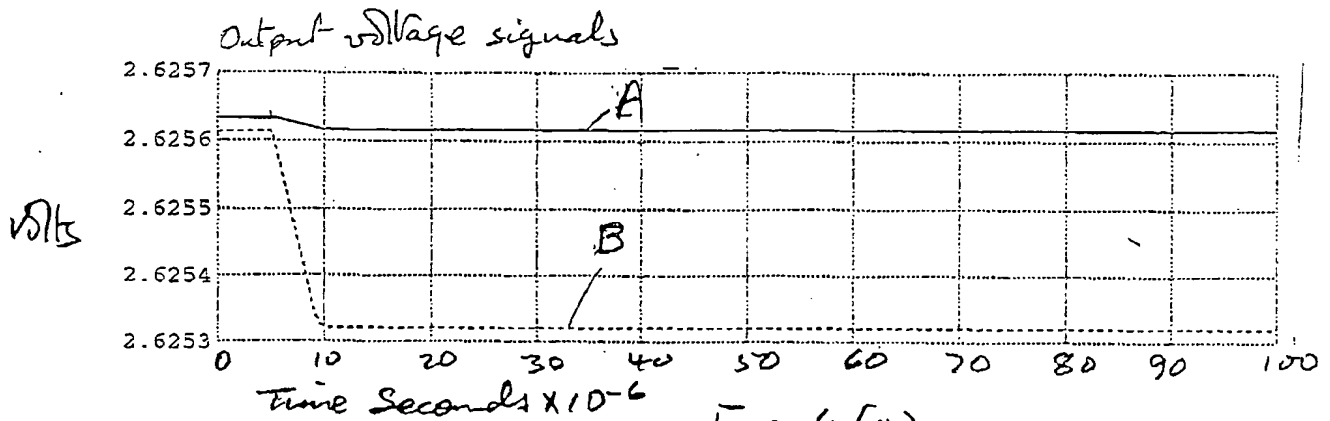


FIG 4(a)

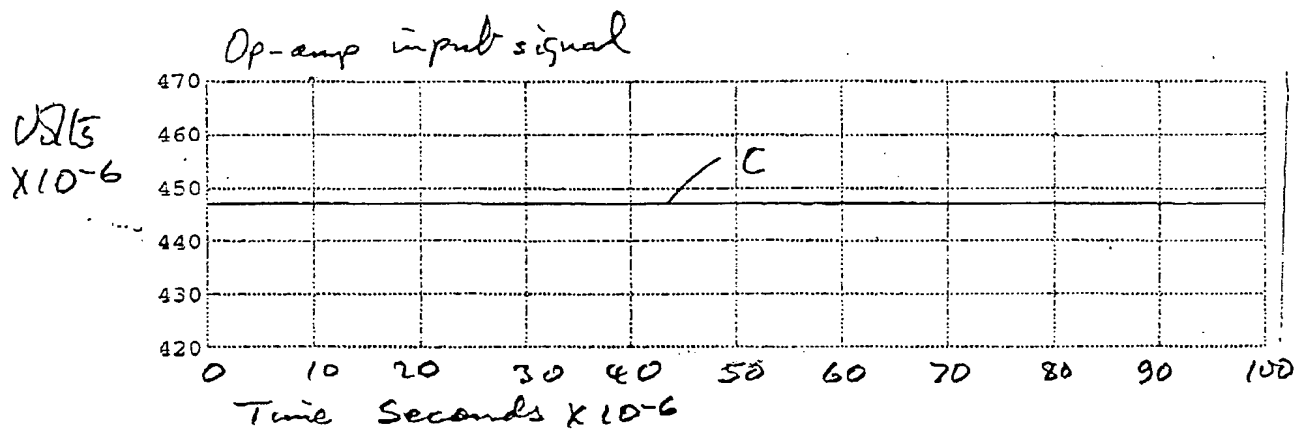


FIG 4(b)

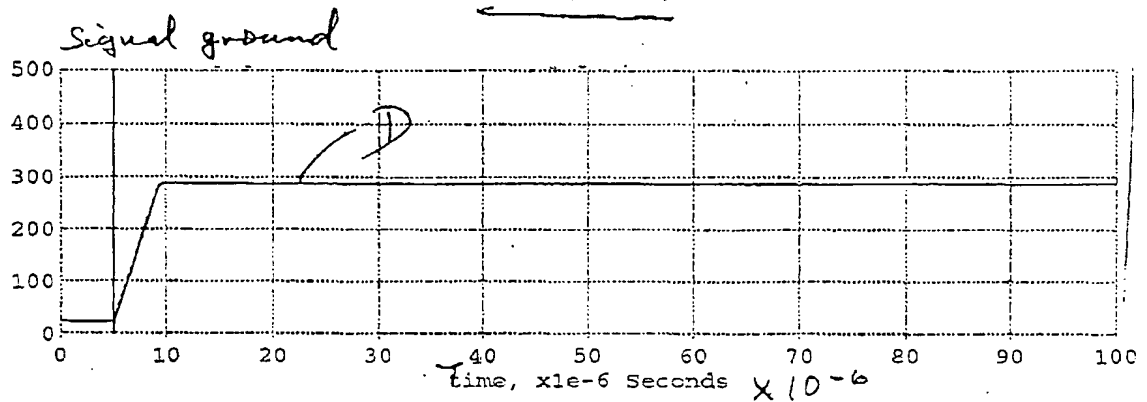


FIG 4(c)

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Output voltage signals

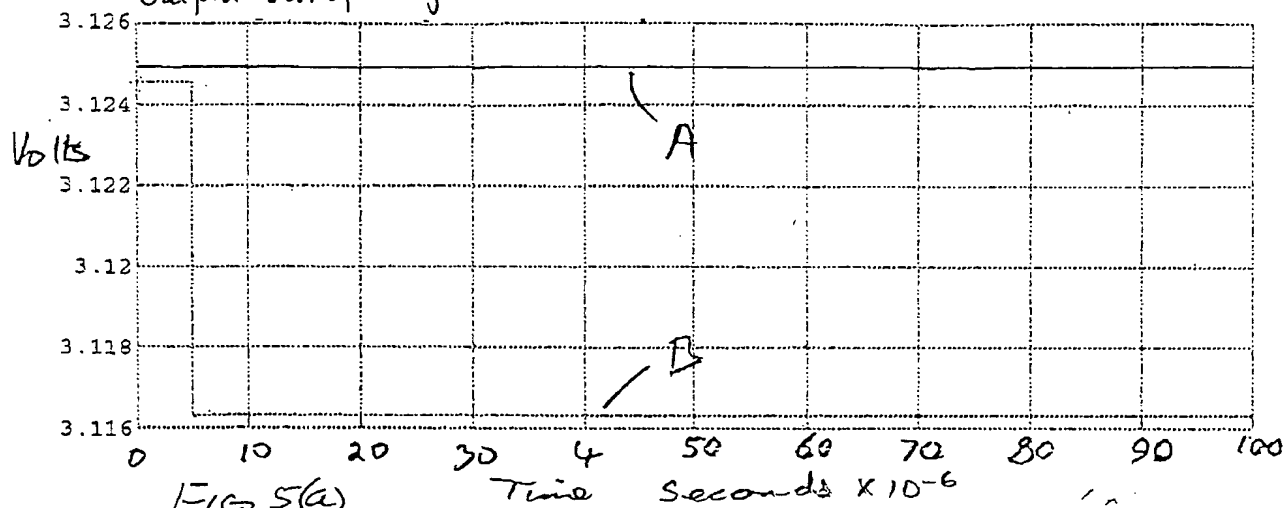


FIG 5(a)

Op-amp input signal

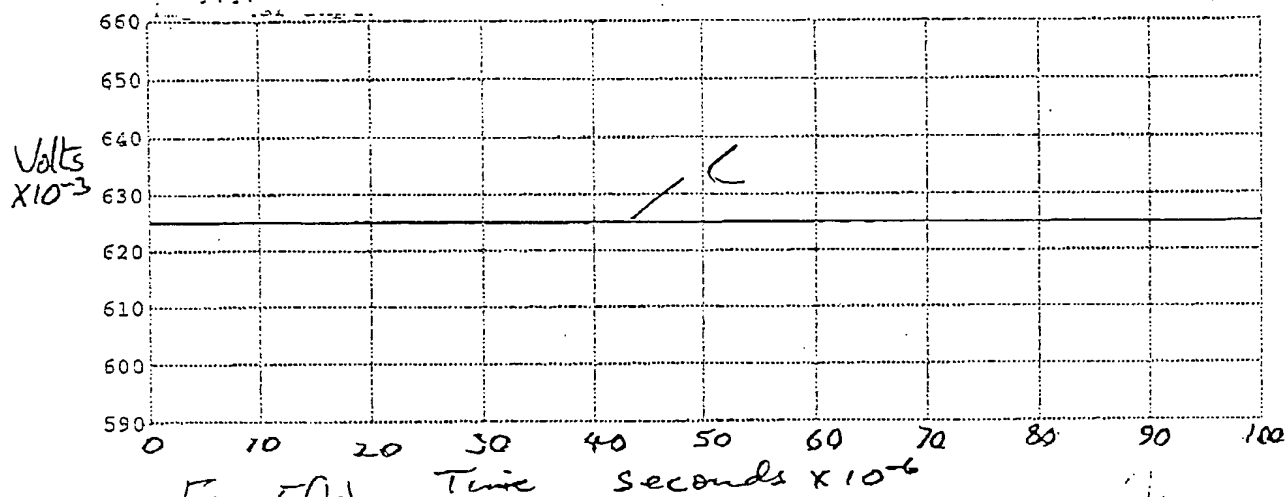


FIG 5(b)

Signal ground

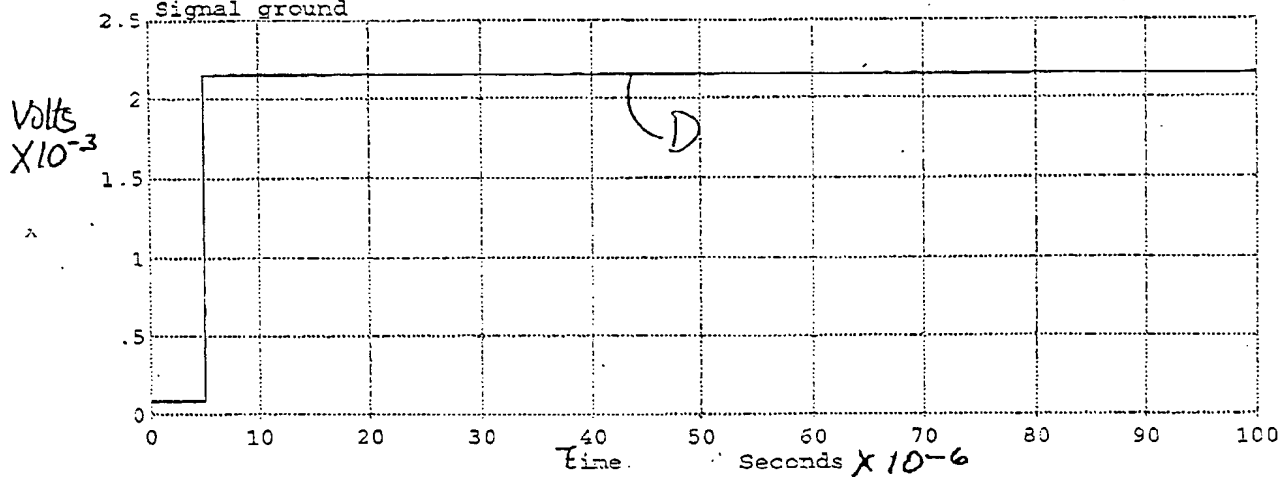


FIG 5(c)